

Notice of References Cited	Application/Control No. 09/838,552	Applicant(s)/Patent Under Reexamination BABAIA ET AL.	
	Examiner Mary J. Steelman	Art Unit 2122	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,537,559 ✓	07-1996	Kane et al.	712/244
	B	US-5,566,298 ✓	10-1996	Boggs et al.	714/10
	C	US-5,594,905 ✓	01-1997	Mital, Amit	710/260
	D	US-5,832,205 ✓	11-1998	Kelly et al.	714/53
	E	US-6,247,172 B1 ✓	06-2001	Dunn et al.	717/141
	F	US-6,341,324 B1 ✓	01-2002	Caulk et al.	710/260
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Chernoff, A, Herdeg, M, Hookway, R, Reeve, C, Rubin, N, Tye, T, Bharadwaj, Ya S, Yates, J. "FX!32 a profile-directed binary translator", Mar/Apr 1998, IEEE Micro, p. 56-64, retrieved from IEEE 07/07/2004. ✓
	V	Cmelik, Bob, Keppel, David, "Shade: a fast instruction-set simulator for execution profiling", Sigmetrics 5/94, p. 128-137, retrieved from ACM 07/07/2004. ✓
	W	Gschwind, M, Ebcioglu, K, Altman, E, Sathaye, S, "Binary Translation and Architecture Convergence Issues for IBM System/390", IBM T.J. Watson Research Center, 1999, p. 1-12, <URL: http://www.research.ibm.com/vliw/Pdf/ics00.pdf >, retrieved 07/07/2004. ✓
	X	Moon, S.M., Chung, H.M., Park, J., Shim, S.M., Ahn, J.W., "SPARC-based VLIW testbed", IEE Proc.-Comput. Digkit Tech, Vol. 145, No. 3, May 1998, p. 215-224, retrieved from IEEE 07/07/2004. ✓

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

20